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Patent Number	6,974,779
Issue Date	December 13, 2005
Application Number	10/662875
Filing Date	September 16, 2003
First Named Inventor	David L. O'Meara
Attorney Docket Number	071469-0305306 (RAJ-005)

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Typed or
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Date July 28, 2008	Telephone 480-538-2101
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NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

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STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: Tokyo Electron LimitedApplication No./Patent No.: 6,974,779 Filed/Issue Date: December 13, 2005Entitled: Interfacial Oxidation Process for High-K Gate Dielectric Process Integration

Tokyo Electron Limited, a Corporation
(Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

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- an assignee of less than the entire right, title and interest
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As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the original owner to the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.

[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be submitted to Assignment Division in accordance with 37 CFR Part 3, to record the assignment in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

July 28, 2008

Date

Signature

Tugrul Yasar

480-539-2101

Printed or Typed Name

Telephone Number

Vice President of Intellectual Property

Title

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